

ASSIGNMENT 5: LOGIC FAMILY

1. Explain the classification of logic families.
2. Define Positive and negative logic, Propagation Delay, Power Dissipation, Noise Margin and Noise Immunity, fan in and fan out, current sinking and sourcing.
3. Explain TTL with 2 input NAND Gate with totem pole output.
4. Explain CMOS logic with its transfer characteristic
5. Explain Macro operations vs micro operations.

