

DEPARTMENT: ELECTRICAL

SEMESTER: 3RD

SUBJECT NAME: ANALOG AND DIGITAL ELECTRONICS

SUBJECT CODE: 3130907
FACULTY NAME: SUNIL PATEL

ASSIGNMENT: 3 OP-AMP

- 1. Draw the circuit of basic integrator using OP-AMP. What are the problems associated with this configuration? How they are overcome?
- 2. Derive an expression of voltage gain for closed loop non-inverting OP-AMP.
- 3. Draw and explain circuit diagram of first order butterworth low pass filter using OP-AMP.
- 4. What is power amplifier? Give important features of power amplifier circuit.
- 5. Draw and explain Class B Push Pull Amplifier.

